

# **PC355N Series**

# Mini-flat Package, Darlington Phototransistor Output Photocoupler



### **■** Description

**PC355N Series** contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin Mini-flat. Input-output isolation voltage(rms) is 3.75kV. CTR is MIN 600% at input current of 1.0mA.

#### ■ Features

- 1. 4-pin Mini-flat package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. Darlington phototransistor output (CTR : MIN. 600% at  $I_F$ = 1mA,  $V_{CE}$ =2V)
- 4. High isolation voltage between input and output  $(V_{iso(rms)}: 3.75kV)$

#### ■ Agency approvals/Compliance

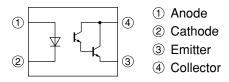
- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC355)
- 2. Package resin : UL flammability grade (94V-0)

#### ■ Applications

- 1. Hybrid substrates that require high density mounting
- 2. Programmable controllers

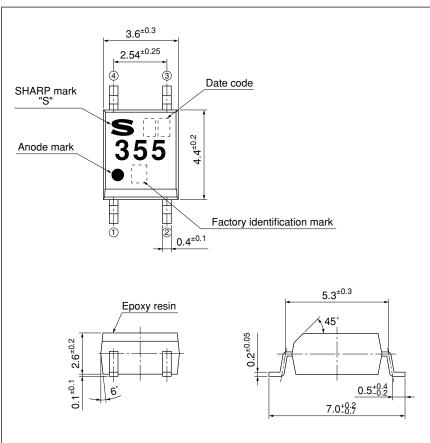


# ■ Internal Connection Diagram



# **■** Outline Dimensions

(Unit: mm)



Product mass: approx. 0.1g



# Date code (2 digit)

	1st o	digit		2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

# Factory identification mark

Factory identification Mark	Country of origin	
no mark	Taman	
	Japan	
	Indonesia	
$\overline{\hspace{1cm}}$	Philippines	
_	China	

<sup>\*</sup> This factory marking is for identification purpose only.

Please contact the local SHARP sales representative to see the actual status of the production.



■ Absolute Maximum Ratings

	■ Absolute Maximum Ratings (T <sub>a</sub> =25°C)							
	Parameter	Symbol	Rating	Unit				
	Forward current	$I_F$	50	mA				
Input	*1 Peak forward current	$I_{FM}$	1	A				
Inj	Reverse voltage	$V_R$	6	V				
	Power dissipation	P	70	mW				
	Collector-emitter voltage	$V_{CEO}$	35	V				
Output	Emitter-collector voltage	$V_{ECO}$	6	V				
Out	Collector current	$I_C$	80	mA				
	Collector power dissipation	$P_{C}$	150	mW				
-	Total power dissipation P <sub>tot</sub> 170		170	mW				
(	Operating temperature	$T_{opr}$	-30 to +100	°C				
Storage temperature		$T_{stg}$	-40 to +125	°C				
*2 Isolation voltage		$V_{iso\ (rms)}$	3.75	kV				
*3 (	Soldering temperature	$T_{sol}$	260	°C				

<sup>\*1</sup> Pulse width≤100µs, Duty ratio : 0.001 \*2 40 to 60%RH, AC for 1 minute, f=60Hz \*3 For 10s

# **■** Electro-optical Characteristics

 $(T_a=25^{\circ}C)$ 

								( "
	Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage		$V_F$	$I_F=20mA$	_	1.2	1.4	V
Input	Reverse Current		$I_R$	$V_R=4V$	_	_	10	μΑ
	Terminal capacitance		$C_t$	V=0, f=1kHz	_	30	250	pF
	Collector dark	current	$I_{CEO}$	$V_{CE}=10V, I_{F}=0$	_	_	1000	nA
Output	Collector-emitter breakdown voltage		BV <sub>CEO</sub>	$I_{C}=0.1 \text{mA}, I_{F}=0$	35	_	-	V
	Emitter-collector breakdown voltage		$BV_{ECO}$	$I_{E}=10\mu A, I_{F}=0$	6	-	_	V
	Collector curr	ent	$I_{C}$	$I_F=1mA$ , $V_{CE}=2V$	6	16	75	mA
Transfer characteristics	Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_F=20\text{mA}, I_C=1\text{mA}$	_	0.8	1.0	V
	Isolation resistance		R <sub>ISO</sub>	DC500V, 40 to 60%RH	5×10 <sup>10</sup>	1×10 <sup>11</sup>	-	Ω
	Floating capacitance		$C_{\mathrm{f}}$	V=0, f=1MHz	_	0.6	1.0	pF
	Desmanes time	Rise time	$t_r$	V 2V V 2 A D 1000	_	60	300	μs
	Response time	Fall time	$t_{\mathrm{f}}$	$V_{CE}$ =2V, $I_{C}$ =2mA, $R_{L}$ =100 $\Omega$	-	53	250	μs



# ■ Model Line-up

Doolsogo	Taping			
Package	3 000 pcs/reel	750 pcs/reel		
Model No.	PC355N	PC355NT		

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.

Sheet No.: D2-A00801EN



Fig.1 Forward Current vs. Ambient Temperature

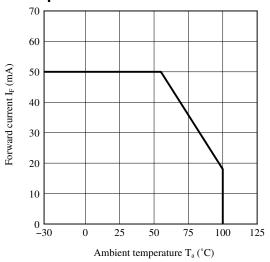


Fig.3 Collector Power Dissipation vs.
Ambient Temperature

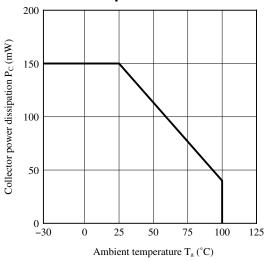


Fig.5 Peak Forward Current vs. Duty Ratio

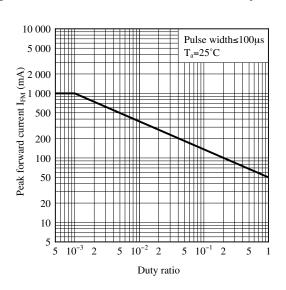


Fig.2 Diode Power Dissipation vs. Ambient Temperature

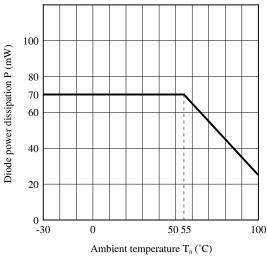


Fig.4 Total Power Dissipation vs. Ambient Temperature

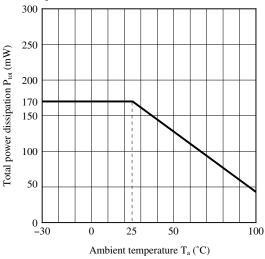


Fig.6 Forward Current vs. Forward Voltage

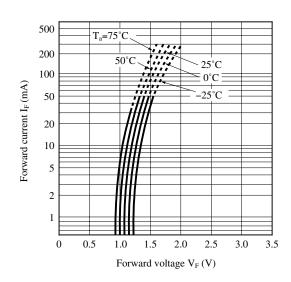




Fig.7 Current Transfer Ratio vs. Forward Current

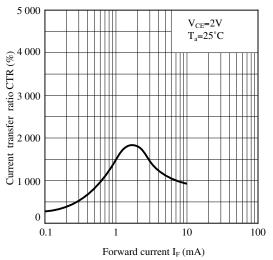


Fig.9 Relative Current Transfer Ratio vs.

Ambient Temperature

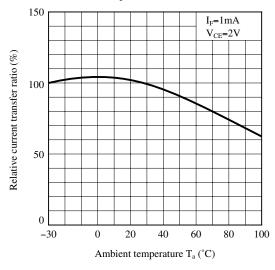


Fig.11 Collector Dark Current vs. Ambient Temperature

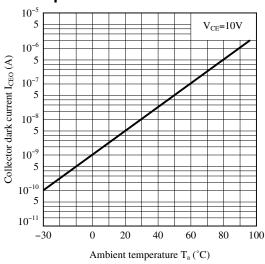


Fig.8 Collector Current vs. Collector-emitter Voltage

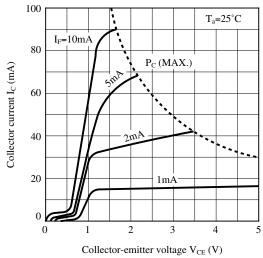


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

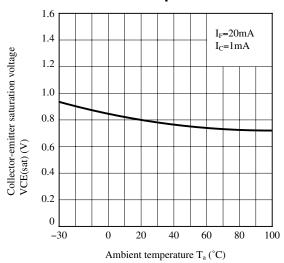


Fig.12 Response Time vs. Load Resistance

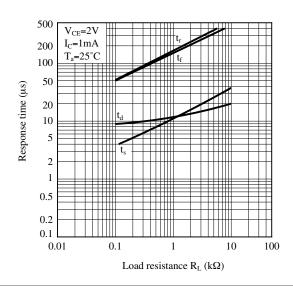
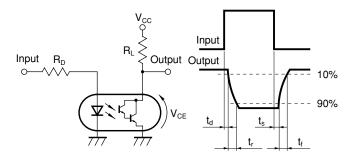


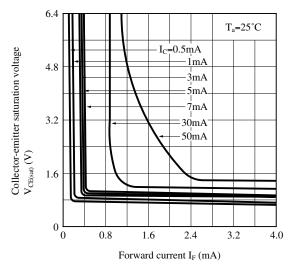


Fig.13 Test Circuit for Response Time



Please refer to the conditions in Fig.12

Fig.14 Collector-emitter Saturation Voltage vs. Forward Current



Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.



#### ■ Design Considerations

# Design guide

While operating at I<sub>F</sub><1.0mA, CTR variation may increase.

Please make design considering this fact.

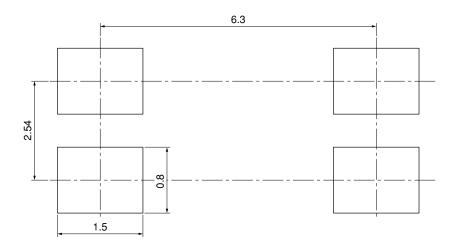
This product is not designed against irradiation and incorporates non-coherent IRED.

### Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

# Recommended Foot Print (reference)



(Unit: mm)

<sup>☆</sup> For additional design assistance, please review our corresponding Optoelectronic Application Notes.



#### ■ Manufacturing Guidelines

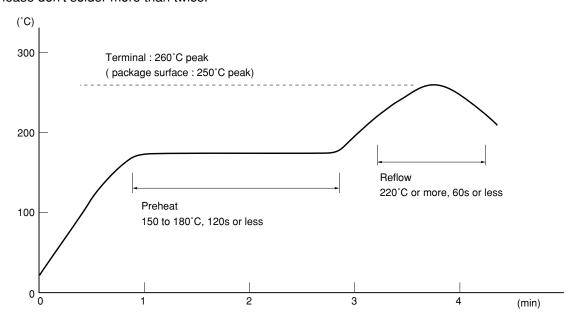
### Soldering Method

#### Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



#### Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

#### Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

#### Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



#### Cleaning instructions

#### Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

#### Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

#### Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

#### Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

Sheet No.: D2-A00801EN



# ■ Package specification

# ● Tape and Reel package

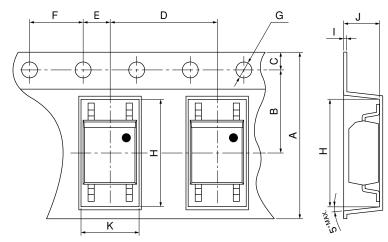
1. 3 000pcs/reel Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

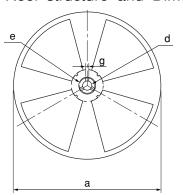
Reel: PS

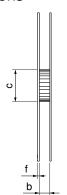
### Carrier tape structure and Dimensions



Dimensions List (Unit: mm)						
A	В	С	D	Е	F	G
12.0 <sup>±0.3</sup>	5.5 <sup>±0.1</sup>	1.75 <sup>±0.1</sup>	8.0 <sup>±0.1</sup>	2.0 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>	φ1.5 <del>+</del> 0.1
Н	I	J	K			
7.4 <sup>±0.1</sup>	0.3 <sup>±0.05</sup>	3.1 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>			

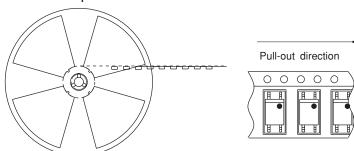
# Reel structure and Dimensions





Dimensio	ns List	(Unit: mm)		
a	b	c	d	
370	13.5 <sup>±1.5</sup>	80±1.0	13±0.5	
e	f	g		
21 <sup>±1.0</sup>	2.0 <sup>±0.5</sup>	2.0 <sup>±0.5</sup>		

# Direction of product insertion



[Packing: 3 000pcs/reel]



# 2. 750pcs/reel

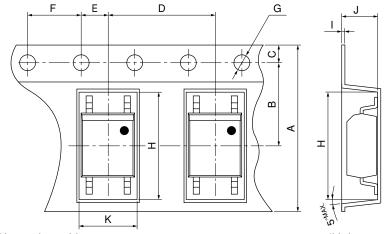
#### Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

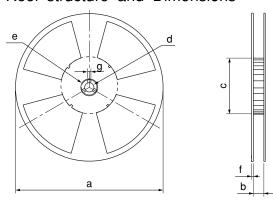
Reel: PS

# Carrier tape structure and Dimensions



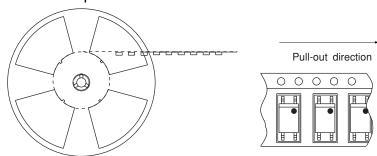
Di	Dimensions List (Unit: mm						
	A	В	С	D	Е	F	G
	12.0±0.3	5.5 <sup>±0.1</sup>	1.75 <sup>±0.1</sup>	8.0 <sup>±0.1</sup>	2.0 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>	φ1.5 <sup>+0.1</sup>
	Н	I	J	K			
	7 4+0.1	0.2+0.05	2.1+0.1	4.0+0.1			

#### Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	c	d	
180	13.5 <sup>±1.5</sup>	80 <sup>±1.0</sup>	13 <sup>±0.5</sup>	
e	f	g		
21 <sup>±1.0</sup>	2.0±0.5	2.0±0.5		

# Direction of product insertion



[Packing: 750pcs/reel]



#### ■ Important Notices

- · The circuit application examples in this publication are provided to explain representative applications of SHARP devices and are not intended to guarantee any circuit design or license any intellectual property rights. SHARP takes no responsibility for any problems related to any intellectual property right of a third party resulting from the use of SHARP's devices.
- · Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device. SHARP reserves the right to make changes in the specifications, characteristics, data, materials, structure, and other contents described herein at any time without notice in order to improve design or reliability. Manufacturing locations are also subject to change without notice.
- · Observe the following points when using any devices in this publication. SHARP takes no responsibility for damage caused by improper use of the devices which does not meet the conditions and absolute maximum ratings to be used specified in the relevant specification sheet nor meet the following conditions:
- (i) The devices in this publication are designed for use in general electronic equipment designs such as:
  - --- Personal computers
  - --- Office automation equipment
  - --- Telecommunication equipment [terminal]
  - --- Test and measurement equipment
  - --- Industrial control
  - --- Audio visual equipment
  - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

with equipment that requires higher reliability such as:

- --- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
- (iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:
  - --- Space applications
  - --- Telecommunication equipment [trunk lines]
  - --- Nuclear power control equipment
  - --- Medical and other life support equipment (e.g., scuba).
- · If the SHARP devices listed in this publication fall within the scope of strategic products described in the Foreign Exchange and Foreign Trade Law of Japan, it is necessary to obtain approval to export such SHARP devices.
- This publication is the proprietary product of SHARP and is copyrighted, with all rights reserved. Under the copyright laws, no part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, for any purpose, in whole or in part, without the express written permission of SHARP. Express written permission is also required before any use of this publication may be made by a third party.
- · Contact and consult with a SHARP representative if there are any questions about the contents of this publication.