# **High Voltage Transistors NPN Silicon**

# COLLECTOR 3 BASE 1 EMITTER

#### **MAXIMUM RATINGS**

Rating	Symbol	MPSA42	MPSA43	Unit
Collector-Emitter Voltage	VCEO	300	200	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	300	200	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	6.0	Vdc
Collector Current — Continuous	IC	500		mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12		Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		°C

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	200	°C/mW
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/mW

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

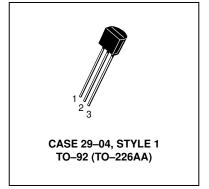
Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage <sup>(1)</sup> (IC = 1.0 mAdc, IB = 0)	MPSA42 MPSA43	V <sub>(BR)</sub> CEO	300 200		Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 100 μAdc, I <sub>E</sub> = 0)	MPSA42 MPSA43	V <sub>(BR)</sub> CBO	300 200		Vdc
Emitter-Base Breakdown Voltage (IE = 100 μAdc, IC = 0)		V <sub>(BR)EBO</sub>	6.0	_	Vdc
Collector Cutoff Current ( $V_{CB} = 200 \text{ Vdc}, I_{E} = 0$ ) ( $V_{CB} = 160 \text{ Vdc}, I_{E} = 0$ )	MPSA42 MPSA43	ICBO	_ _	0.1 0.1	μAdc
Emitter Cutoff Current $(V_{EB} = 6.0 \text{ Vdc}, I_{C} = 0)$ $(V_{EB} = 4.0 \text{ Vdc}, I_{C} = 0)$	MPSA42 MPSA43	IEBO	_ _	0.1 0.1	μAdc

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.



\*Motorola Preferred Device





### MPSA42 MPSA43

# $\textbf{ELECTRICAL CHARACTERISTICS} \ (T_{A} = 25^{\circ}\text{C unless otherwise noted}) \ (\text{Continued})$

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS <sup>(1)</sup>		•			
DC Current Gain $ \begin{array}{l} (I_C=1.0 \text{ mAdc, V}_{CE}=10 \text{ Vdc)} \\ (I_C=10 \text{ mAdc, V}_{CE}=10 \text{ Vdc)} \\ (I_C=30 \text{ mAdc, V}_{CE}=10 \text{ Vdc)} \end{array} $		hFE	25 40 40	_ _ _	_
Collector-Emitter Saturation Voltage (IC = 20 mAdc, IB = 2.0 mAdc)	MPSA42 MPSA43	V <sub>CE(sat)</sub>	— —	0.5 0.4	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 20 mAdc, I <sub>B</sub> = 2.0 mAdc)		V <sub>BE(sat)</sub>	_	0.9	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain — Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)		fΤ	50	_	MHz
Collector-Base Capacitance (V <sub>CB</sub> = 20 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	MPSA42 MPSA43	C <sub>cb</sub>	_ _	3.0 4.0	pF

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu s,$  Duty Cycle  $\leq$  2.0%.

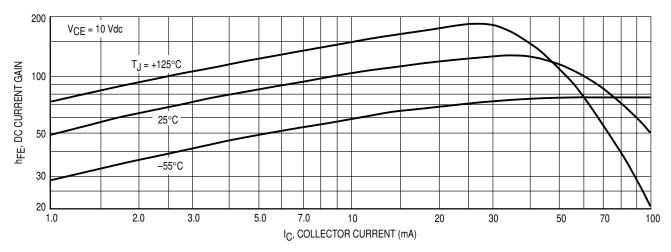


Figure 1. DC Current Gain

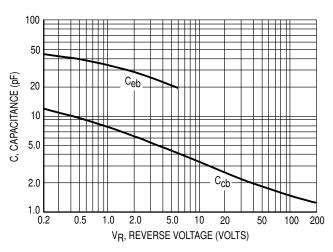


Figure 2. Capacitances

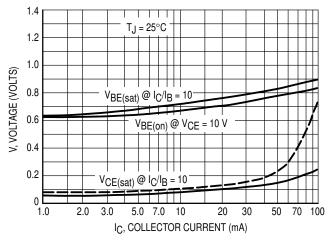


Figure 4. "On" Voltages

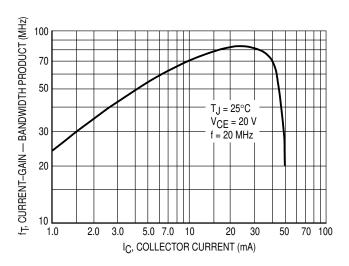


Figure 3. Current-Gain — Bandwidth Product

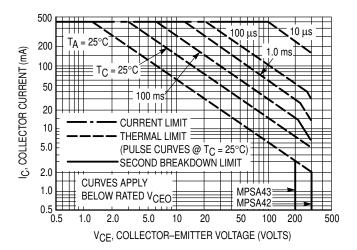
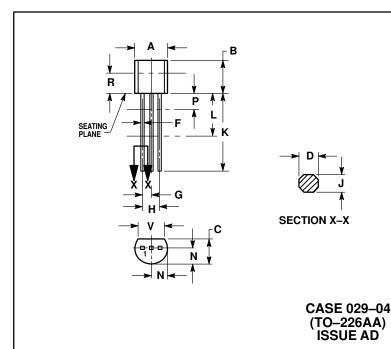


Figure 5. Maximum Forward Bias Safe Operating Area

#### PACKAGE DIMENSIONS



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

  DIMENSION F APPLIES BETWEEN P AND L.
- DIMENSION P APPLIES BETWEEN F AIND L.
  DIMENSION D AND J APPLY BETWEEN L AND K
  MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3 43	

STYLE 1: PIN 1. EMITTER BASE 3. COLLECTOR

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