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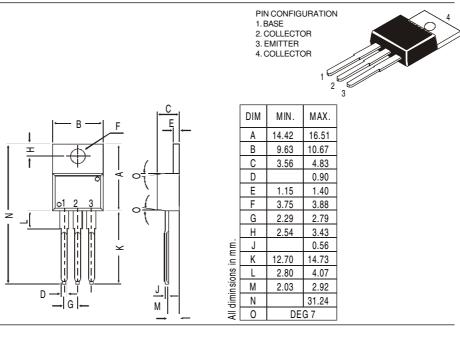


TO-220 Plastic Package

BD241, BD241A, BD241B, BD241C BD242, BD242A, BD242B, BD242C

241 241A 241B 241C

BD241, 241A, 241B, 241C NPN PLASTIC POWER TRANSISTORS BD242, 242A, 242B, 242C PNP PLASTIC POWER TRANSISTORS General Purpose Amplifier and Switching Applications



ABSOLUTE MAXIMUM RATINGS

		242	242A	242B	242 <i>C</i>	
Collector-emitter voltage ($V_{BE} = 0$)	V_{CES}	max. 55	70	90	115	V
Collector-emitter voltage (open base)	V_{CEO}	max. 45	60	80	100	V
Collector current	I_C	max.	5.	.0		Α
Total power dissipation up to $T_C = 25^{\circ}C$	P _{tot}	max.	4	0		W
Junction temperature	T_{j}	max.	15	50		°C
Collector-emitter saturation voltage	,					
$I_{\rm C} = 3 \ A; \ I_{\rm B} = 0.6 \ A$	V CEsat	max.	1.	.2		V
D.C. current gain						
$I_C = 1 A; V_{CE} = 4 V$	h_{FE}	min.	2	5		
RATINGS (at $T_A=25^{\circ}$ C unless otherwise s	specified)					
Limiting values		241	241A	241B	241C	
		242	242A	242B	242 <i>C</i>	
Collector-emitter voltage $(V_{PP} = 0)$	VCEC	max. 55	70	90	115	V

		242	242A	242B	242 <i>C</i>	
Collector-emitter voltage ($V_{BE} = 0$)	V_{CES}	max. 55	70	90	115	V
Collector-emitter voltage (open base)	V_{CEO}	max. 45	60	80	100	V
Collector-emitter voltage ($R_{BE} = 100\Omega$)	V_{CER}	max. 55	70	90	115	V

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Emitter-base voltage (open collector)	V_{EBO}	max.	5.0	V
Collector current	I_C	max.	3.0	Α
Collector current (Peak value)	I_C	max.	5.0	Α
Base current	IB	max.	1.0	Α
Total power dissipation upto T _C =25°C	P _{tot}	max.	40	W
Derate above 25°C		max.	0.32	₩°C
Junction temperature	T_i	max.	150	°C
Storage temperature	Τ΄ _{stg}		-65 to +150	${}^{\mathfrak{C}}$
THERMAL RESISTANCE				
From junction to case	R _{th j-c}		3.125	°C/W
From junction to ambient	R _{th j–a}		62.5	°Ç/W

CHARACTERISTICS

 $T_{amb} = 25^{\circ}C$ unless otherwise specified

242 242A 242B 242C Collector cutoff current $I_B = 0; V_{CE} = 30 V$ ICEO max. 0.3 0.3 – _ mA $I_B = 0; V_{CE} = 60 V$ max. – - 0.3 0.3 mAICEO $V_{BE} = 0; V_{CE} = V_{CEO}$ Emitter cut-off current max. 0.2 mAICES $I_{\rm C} = 0; V_{EB} = 5 V$ 1.0 IEBO max. mABreakdown voltages 60 80 70 90 $I_C = 30 mA; I_B = 0$ $V_{CEO(sus)}^*$ min. 45 100 V $I_C = 1 mA; V_{BE} = 0$ V_{CES} min. 55 115 V $I_E = 1 mA; I_C = 0$ V5.0 V_{EBO} min. Saturation voltage $I_C = 3 A; I_B = 0.6 A$ V_{CEsat}* max. 1.2 VBase emitter on voltage $I_{C} = 3 A; V_{CE} = \bar{4} V$ VBE(on)* max. 1.8 VD.C. current gain $I_C = 1 A; V_{CE} = 4 V$ h_{FE}^* min. 25 $I_C = 3 \; A; \; V_{CE} = 4 \; V$ 10 h_{FE}^* min. Transition frequency $I_{C} = 0.5 \text{ A}; V_{CE} = 10 \text{ V}; f = 1 \text{ MHz}$ f_T (1) 3 MHz min. Small signal current gain $I_C = 0.5A; V_{CE} = 10V; f = 1 KHz$ hfe min. 20

* Pulse test: pulse width $\leq 300 \ \mu s$; duty cycle $\leq 2\%$ (1) $f_T = |h_{fe}| \cdot f_{test}$ **Notes**

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Data Sheet