

Vishay Semiconductors

Fast Avalanche Sinterglass Diode

Features

- · Glass passivated junction
- · Hermetically sealed package
- Low reverse current
- · Soft recovery characteristics
- · Controlled avalanche characteristics
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



Fast "soft recovery" rectification diode





Mechanical Data

Case: SOD-64 Sintered glass case

Terminals: Plated axial leads, solderable per

MIL-STD-750, Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any Weight: approx. 858 mg

Parts Table

Part	Type differentiation	Package		
BYT77	V _R = 800 V; I _{FAV} = 3 A	SOD-64		
BYT78	V _R = 1000 V; I _{FAV} = 3 A	SOD-64		

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

Tamb - 20 0; dilicos cinci mos	opeomea				
Parameter	Test condition	Part	Symbol	Value	Unit
Reverse voltage	see electrical characteristics	BYT77	$V_R = V_{RRM}$	800	V
		BYT78	$V_R = V_{RRM}$	1000	V
Peak forward surge current	t _p = 10 ms, half sinewave		I _{FSM}	100	Α
Average forward current	T _{amb} ≤ 45 °C		I _{FAV}	3	Α
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to +175	°C
Non repetitive reverse avalanche energy	I _{(BR)R} = 0.4 A		E _R	10	mJ

Maximum Thermal Resistance

 T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Junction ambient	I = 10 mm, T _L = constant	R_{thJA}	25	K/W
	on PC board with spacing 25 mm	R _{thJA}	70	K/W

Document Number 86034 www.vishay.com

Vishay Semiconductors



Electrical Characteristics

 T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward voltage	I _F = 3 A	V _F		1.0	1.2	V
Reverse current	$V_R = V_{RRM}$	I _R		1	5	μА
	V _R = V _{RRM} , T _j = 150 °C	I _R		60	150	μА
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$	t _{rr}			250	ns

Typical Characteristics (Tamb = 25 °C unless otherwise specified)

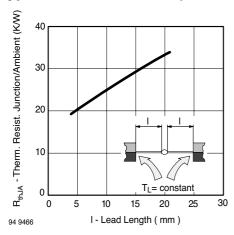


Figure 1. Max. Thermal Resistance vs. Lead Length

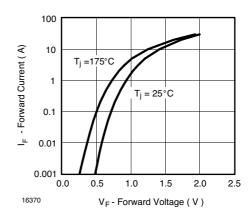


Figure 3. Forward Current vs. Forward Voltage

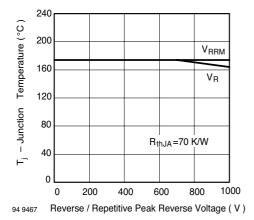


Figure 2. Junction Temperature vs. Reverse/Repetitive Peak Reverse Voltage

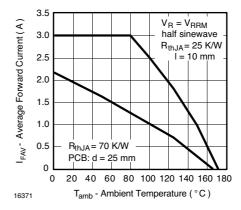


Figure 4. Max. Average Forward Current vs. Ambient Temperature



Vishay Semiconductors

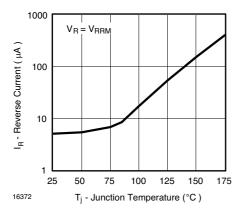


Figure 5. Reverse Current vs. Junction Temperature

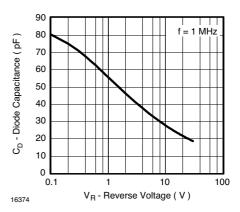


Figure 7. Diode Capacitance vs. Reverse Voltage

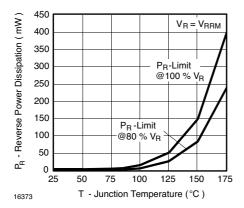
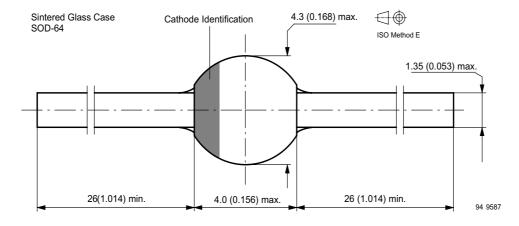


Figure 6. Max. Reverse Power Dissipation vs. Junction Temperature

Package Dimensions in mm (Inches)



BYT77 / BYT78

Vishay Semiconductors



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

> We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

www.vishay.com Rev. 1.7, 13-Apr-05

Legal Disclaimer Notice



Vishay

Notice

Specifications of the products displayed herein are subject to change without notice. Vishay Intertechnology, Inc., or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Vishay's terms and conditions of sale for such products, Vishay assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of Vishay products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Vishay for any damages resulting from such improper use or sale.

www.vishay.com Revision: 08-Apr-05