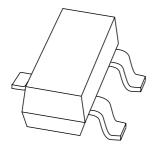
# **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# **BF840**NPN medium frequency transistor

Product data sheet Supersedes data of 1999 Apr 12 2004 Jan 13



NXP Semiconductors Product data sheet

# NPN medium frequency transistor

**BF840** 

#### **FEATURES**

- Low current (max. 25 mA)
- Low voltage (max. 40 V).

#### **APPLICATIONS**

- AM mixers
- IF amplifiers in AM/FM receivers.

#### **DESCRIPTION**

NPN medium frequency transistor in a SOT23 plastic package.

#### **MARKING**

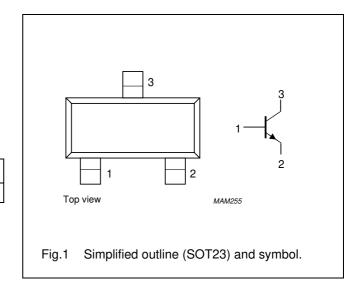
TYPE NUMBER	MARKING CODE <sup>(1)</sup>
BF840	NC*

#### Note

- 1. \* = p : Made in Hong Kong.
  - \* = t : Made in Malaysia.
  - \* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



#### **ORDERING INFORMATION**

TYPE		PACKAGE	
NUMBER	NAME	DESCRIPTION	VERSION
BF840	_	plastic surface mounted package; 3 leads	SOT23

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	40	٧
V <sub>CEO</sub>	collector-emitter voltage	open base	_	40	٧
V <sub>EBO</sub>	emitter-base voltage	open collector	_	4	٧
I <sub>C</sub>	collector current (DC)		_	25	mA
I <sub>CM</sub>	peak collector current		_	25	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

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BF840

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 20 V	_	_	100	nA
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 4 V	_	-	100	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 10 V	67	_	222	
$V_{BE}$	base-emitter voltage	I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 10 V	675	725	775	mV
C <sub>re</sub>	feedback capacitance	I <sub>C</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	-	0.3	_	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 1 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	_	380	_	MHz

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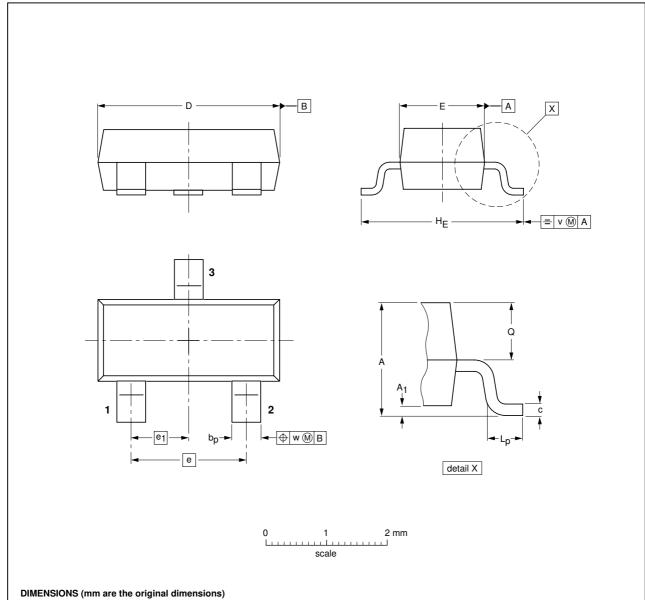
# NPN medium frequency transistor

BF840

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				04-11-04

 $\mathbf{H}_{\mathbf{E}}$ 

 $\mathbf{L}_{\mathbf{p}}$ 

0.45

0.55

0.1

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 $A_1$ 

max.

0.9

bp

0.38

UNIT

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### NPN medium frequency transistor

BF840

#### **DATA SHEET STATUS**

DOCUMENT STATUS(1)	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
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## **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com

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